Fig. 1 (Prior Art)

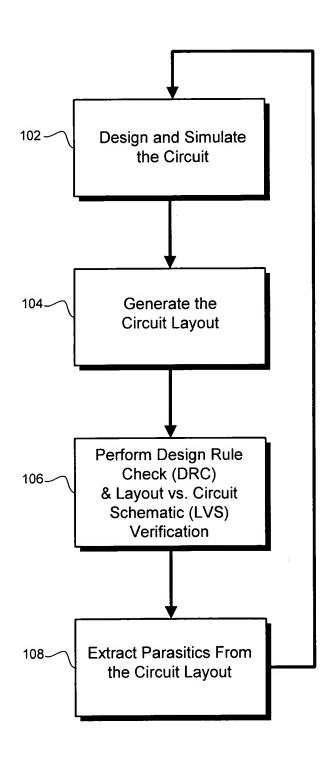
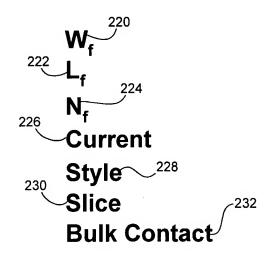
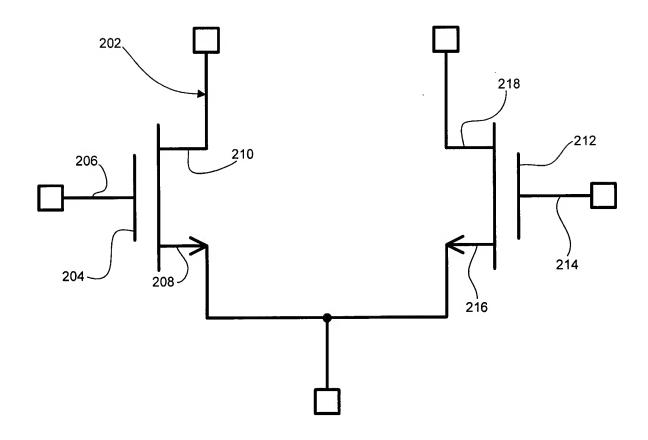


Fig. 2 Differential Pair

Parameters

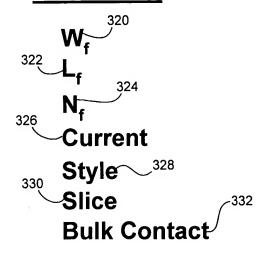


<u>Symbol</u>



HODESTA BITTERS

Parameters



Symbol

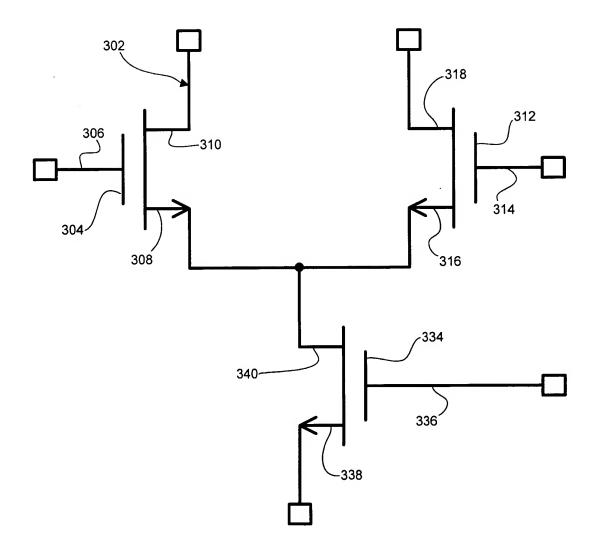
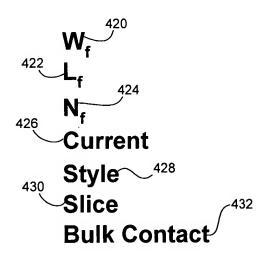


Fig. 4 Current Mirror

Paramet rs



<u>Symbol</u>

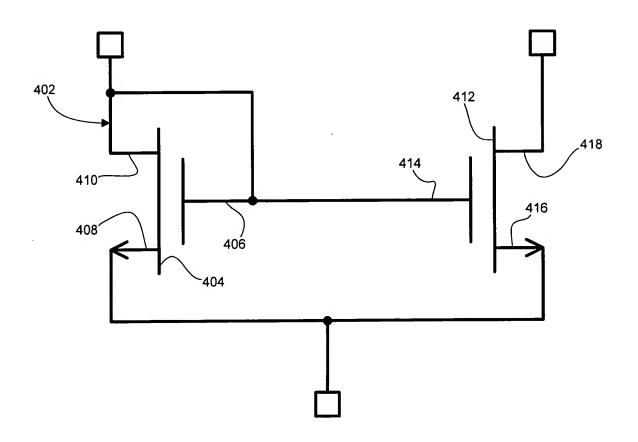
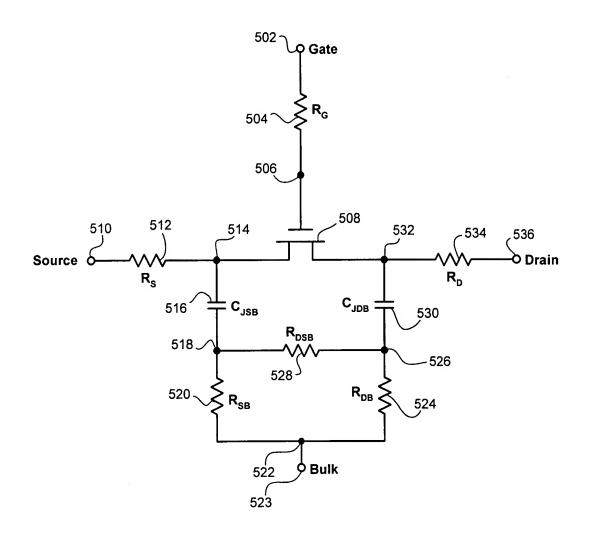
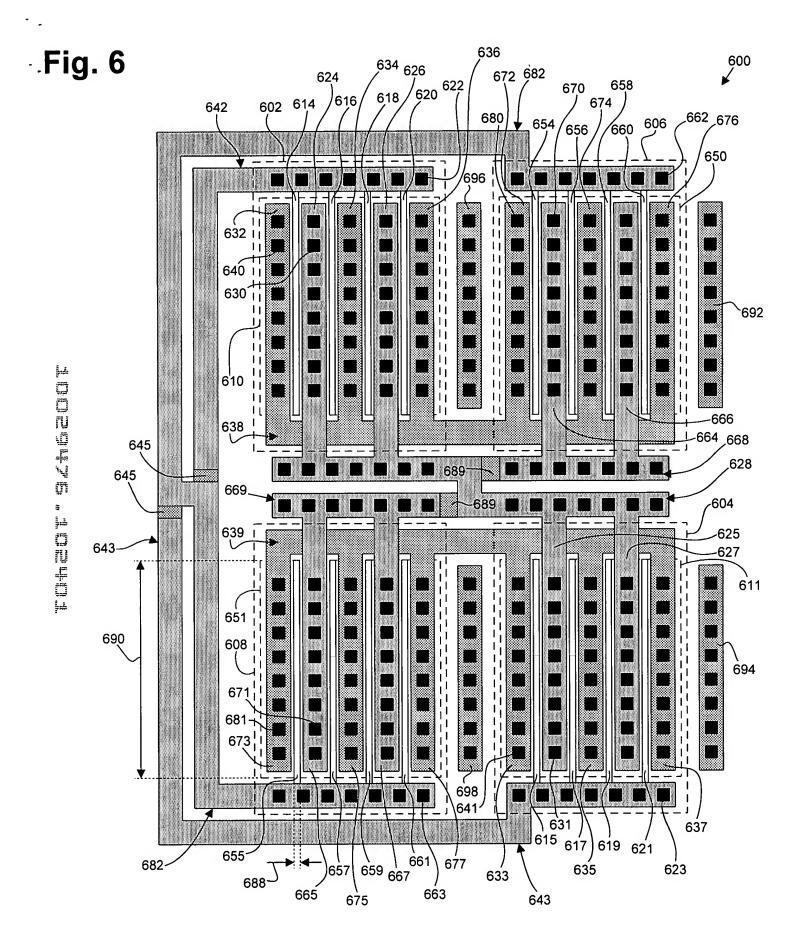


Fig. 5

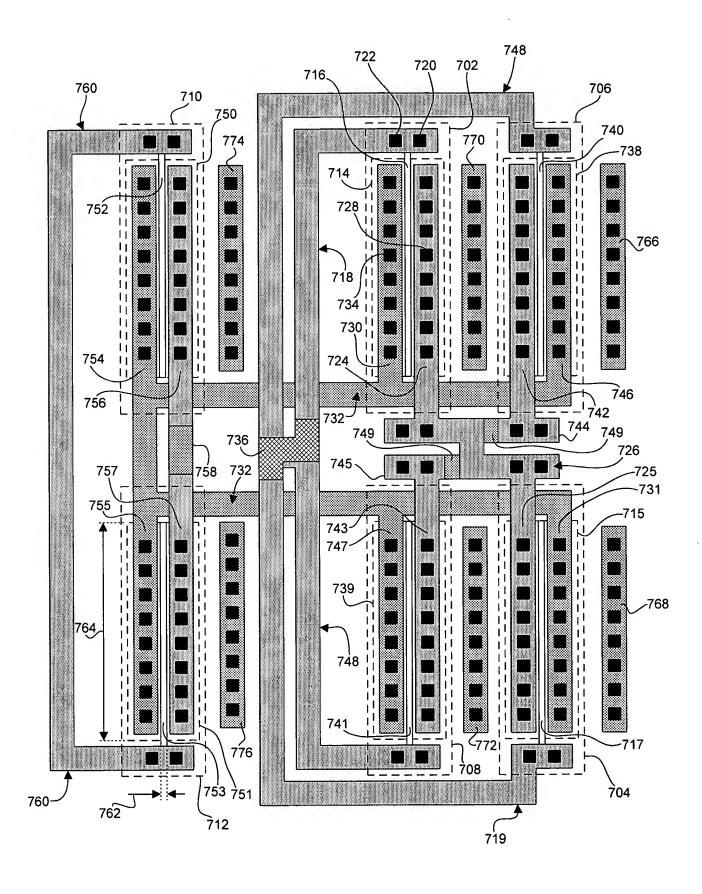
NMOS Transistor Subcircuit Model







√ Fig. 7



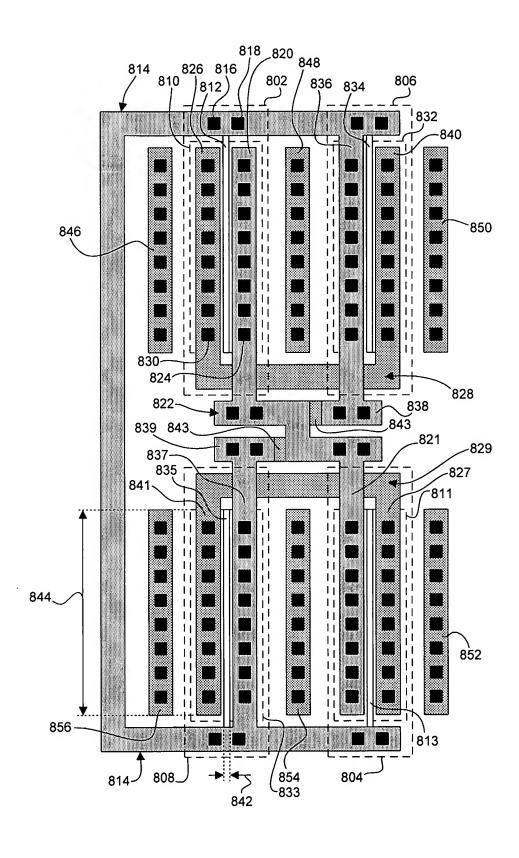
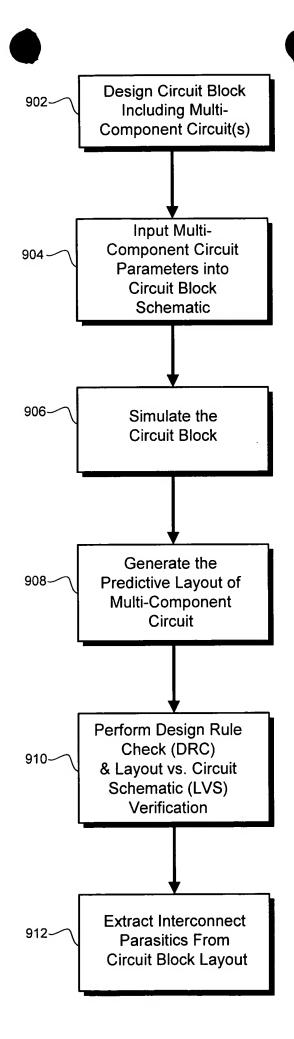


Fig. 9



900

